12th NASA Symposium on VLSI Design

May 26 & 27, 2004
Coeur d'Alene Resort Hotel
Coeur d'Alene, Idaho

Call For Papers

500 Word Summary
Extended Due Date: March 15, 2004
Papers Due: April 28, 2004
Details at *www.cambr.uidaho.edu/symposiums*

Technical Areas

- High Performance Processors
- Low Power and Radiation Tolerant VLSI
- Space and Harsh Environments
- Analog and Mixed Signal
- Programmable Architectures
- Testing and Design Methods
- Emerging Technologies: MEMS, Nanotech

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